



THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )

ELIYAHOU HARARI, ROBERT D. )  
NORMAN and SANJAY MEHROTRA )

Serial No.: 08/931,133 )

Filed: September 18, 1997 )

For: MULTI-STATE FLASH EEPROM )  
SYSTEM WITH CACHE MEMORY )

Group Art Unit: 2785

Examiner: P. Chung

San Francisco, California

Assistant Commissioner of Patents

Washington, D.C. 20231

11/13/1998 DFOKTC 00000005 08931133

01 FC:102 02.00 OP  
02 FC:103 412.00 CH 020.00 OP

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner of Patents, Washington, D.C. 20231 on October 6, 1998.

Brenda J. Dolly

*Brenda J. Dolly* Oct 6, 1998

Signature

Date

Adjustment date: 11/13/1998 DFOKTC  
10/15/1998 AIRBAMIN 00000005 08931133  
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AMENDMENT

02 FC:102 Sir: 902.00 OP

Responsive to the Office Action dated May 20, 1998, please amend the above-identified patent application as follows:

IN THE CLAIMS:

Amend the following existing claims, as follows:

1. (Amended) A method for extending the life of Flash EEPROM memory in a Flash EEPROM system, comprising the steps of:  
temporarily storing data files from a host system intended for the Flash EEPROM memory in a cache memory, said cache memory able to undergo significantly more write/erase cycles than the Flash EEPROM memory;

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